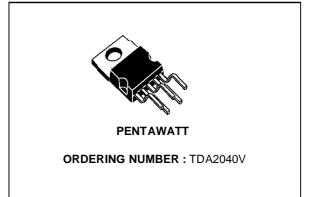


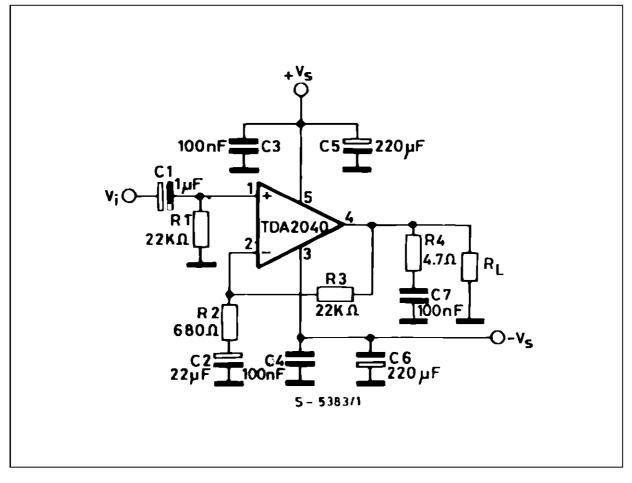
# 20W Hi-Fi AUDIO POWER AMPLIFIER

#### DESCRIPTION

The TDA2040 is a monolithic integrated circuit in Pentawatt ® package, intended for use as an audio class AB amplifier. Typically it provides 22W output power (d = 0.5%) at V<sub>s</sub> = 32V/4 $\Omega$ . The TDA2040 provides high output current and has very low harmonic and cross-over distortion. Further the device incorporates a patented short circuit protection system comprising an arrangement for automatically limiting the dissipated power so as to keep the working point of the output transistors within their safe operating area. A thermal shut-down system is also included.

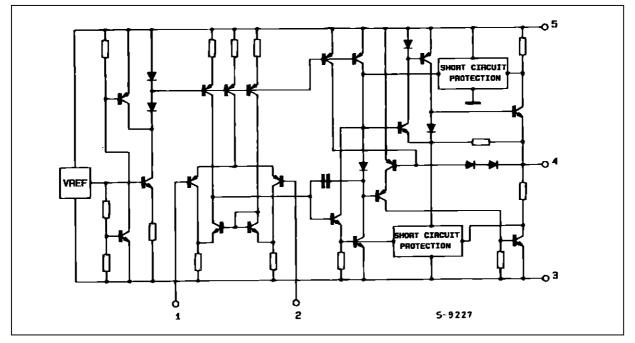


#### **TEST CIRCUIT**

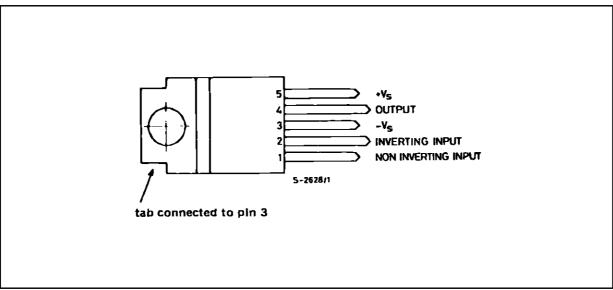


December 1995

# SCHEMATIC DIAGRAM



# **PIN CONNECTION**



# THERMAL DATA

Symbol	Parameter	Value	Unit
R <sub>th j-case</sub>	Thermal Resistance Junction-case Max.	3	°C/W



# ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vs	Supply Voltage	± 20	V
Vi	Input Voltage	Vs	
Vi	Differential Input Voltage	± 15	V
lo	Output Peak Current (internally limited)	4	А
P <sub>tot</sub>	Power Dissipation at T <sub>case</sub> = 75 °C	25	W
T <sub>stg</sub> , T <sub>j</sub>	Storage and Junction Temperature	– 40 to + 150	°C

# ELECTRICAL CHARACTERISTICS

(refer to the test circuit,  $V_S = \pm 16V$ ,  $T_{amb} = 25^{\circ}C$  unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vs	Supply Voltage		± 2.5		± 20	V
l <sub>d</sub>	Quiescent Drain Current	$V_{s} = \pm 4.5V$ $V_{s} = \pm 20V$		45	30 100	mA mA
l <sub>b</sub>	Input Bias Current	$V_s = \pm 20V$		0.3	1	μA
Vos	Input Offset Voltage	$V_s = \pm 20V$		± 2	± 20	mV
l <sub>os</sub>	Input Offset Current				± 200	nA
Po	Output Power	$\begin{array}{l} d=0.5\%, T_{case}=60^\circC\\ f=1kHz & R_L=4\Omega\\ R_L=8\Omega\\ f=15kHz & R_L=4\Omega \end{array}$	20 15	22 12 18		W
BW	Power Bandwidth	$P_0 = 1W, R_L = 4\Omega$		100		kHz
Gv	Open Loop Voltage Gain	f = 1kHz		80		dB
Gv	Closed Loop Voltage Gain	f = 1kHz	29.5	30	30.5	dB
d	Total Harmonic Distortion	$\begin{array}{l} P_{o}=0.1 \text{ to } 10 \text{W}, R_{L}=4 \Omega \\ f=40 \text{ to } 15000 \text{Hz} \\ f=1 \text{kHz} \end{array}$		0.08 0.03		%
e <sub>N</sub>	Input Noise Voltage	B = Curve A B = 22Hz to 22kHz		2 3	10	μV μV
İN	Input Noise Current	B = Curve A B = 22Hz to 22kHz		50 80	200	pА
Ri	Input Resistance (pin 1)		0.5	5		MΩ
SVR	Supply Voltage Rejection	$ \begin{array}{l} R_{L} = 4\Omega,  R_{g} = 22k\Omega,  G_{v} = 30dB \\ f = 100Hz,  V_{ripple} = 0.5V_{RMS} \end{array} $	40	50		dB
η	Efficiency			66 63		%
Tj	Thermal Shut-down Junction Temperature			145		°C



G- 6032 Роит RL=40 Gy=30dB d=0.5% f=1KHz (W) 26 ī 22 18 RL = 8.0 14 10 6 2 5 7 9 11 13 15  $\pm v_s(v)$ 

Figure 1: Output Power versus Supply Voltage

Figure 3: Output Power versus Supply Voltage

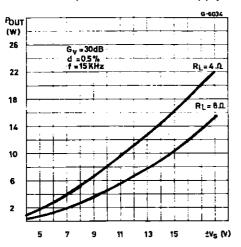


Figure 5 : Supply Voltage Rejection versus Frequency

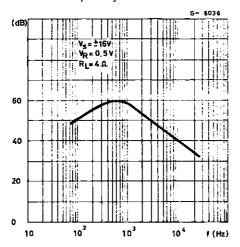


Figure 2: Output Power versus Supply Voltage

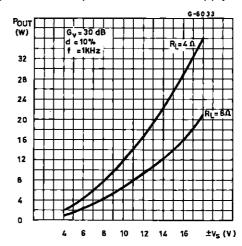


Figure 4: Distortion versus Frequency

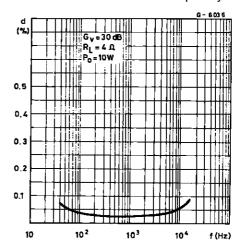


Figure 6: Supply Voltage Rejection versus Voltage Gain

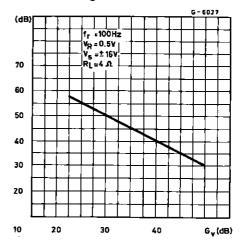




Figure 7 : Quiescent Drain Current versus Supply Voltage

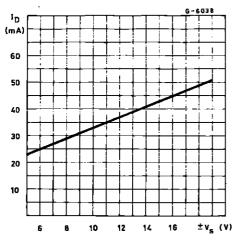


Figure 9 : Power Dissipation versus Output Power

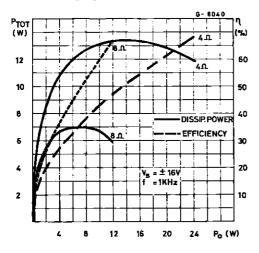
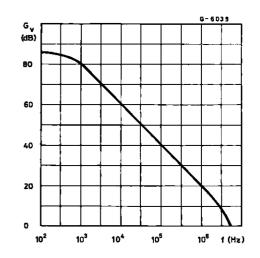
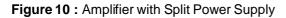


Figure 8 : Open Loop Gain versus Frequency





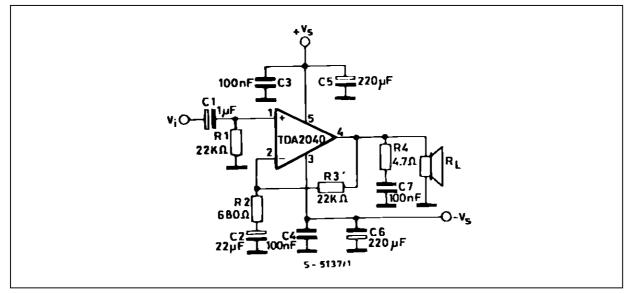
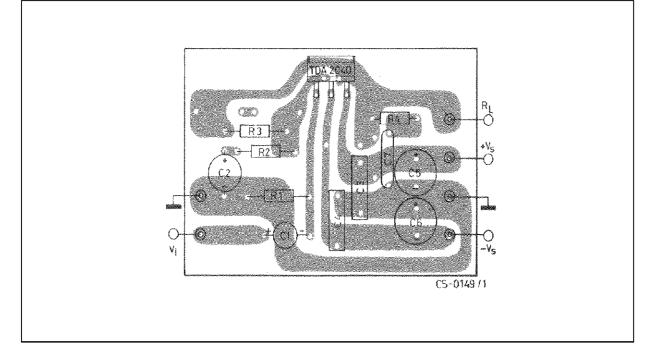


Figure 11 : P.C. Board and Components Layout for the Circuit of Figure 10 (1:1 scale)



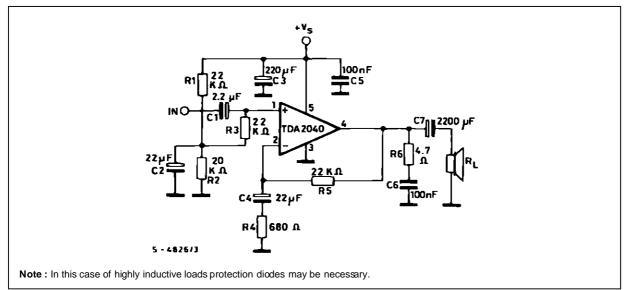
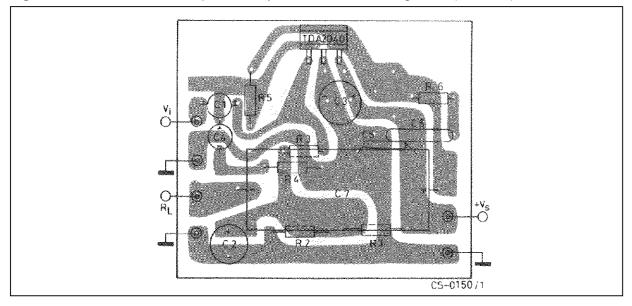


Figure 12 : Amplifier with Split Power Supply (see Note)







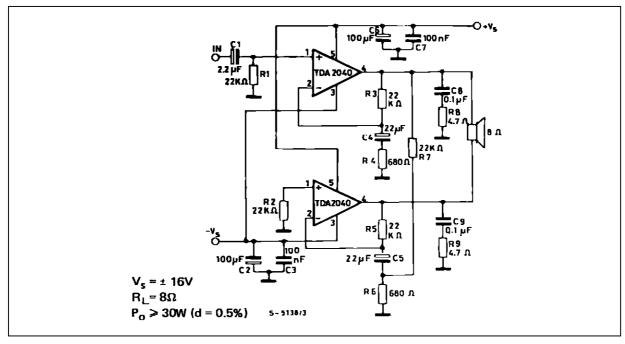
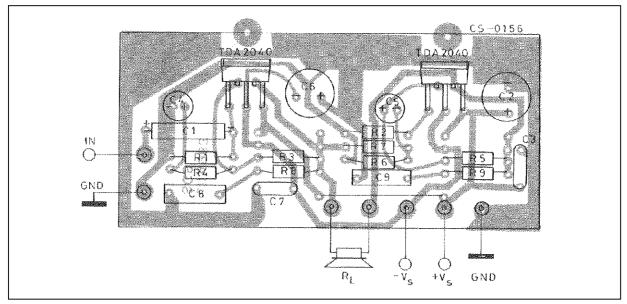


Figure 14 : 30W Bridge Amplifier with Split Power Supply

Figure 15 : P.C. Board and Components Layout for the Circuit of Figure 14 (1:1 scale)





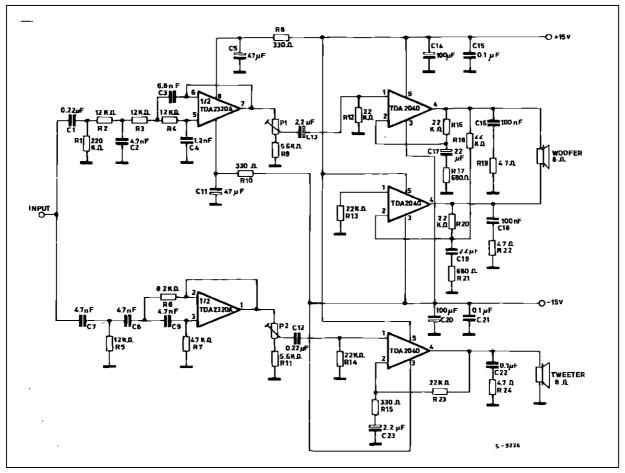
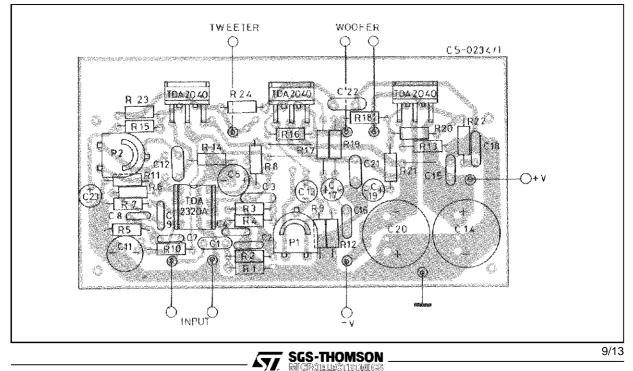


Figure 16 : Two Way Hi-Fi System with Active Crossover





 $(d\theta)$ 1111 WOOFER AMP TWEETER 0 6 W R [= 8.0. -20 -40 10<sup>2</sup> 103 10 10 f (Hz)

Figure 18 : Frequency Response

# MULTIWAY SPEAKER SYSTEMS AND ACTIVE BOXES

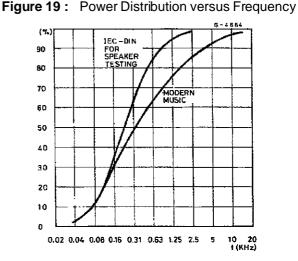
Multiway loudspeaker systems provide the best possible acoustic performance since each loudspeaker is specially designed and optimized to handle a limited range of frequencies. Commonly, these loudspeaker systems divide the audio spectrum into two, three or four bands.

To maintain a flat frequency response over the Hi-Fi audio range the bands covered by each loudspeaker must overlap slightly. Imbalance between the loudspeakers produces unacceptable results therefore it is important to ensure that each unit generates the correct amount of acoustic energy for its segment of the audio spectrum. In this respect it is also important to know the energy distribution of the music spectrum determine the cutoff frequencies of the crossover filters (see Figure 19). As an example, a 100W three-way system with crossover frequencies of 400Hz and 3kHz would require 50W for the woofer, 35W for the midrange unit and 15W for the tweeter.

Both active and passive filters can be used for crossovers but today active filters cost significantly less than a good passive filter using air-cored inductors and non-electrolytic capacitors. In addition, active filters do not suffer from the typical defects of passive filters :

- power loss
- increased impedance seen by the loudspeaker (lower damping)
- difficulty of precise design due to variable loudspeaker impedance

Obviously, active crossovers can only be used if a



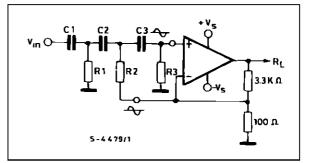
power amplifier is provided for each drive unit. This makes it particularly interesting and economically sound to use monolithic power amplifiers. In some applications, complex filters are not really necessary and simple RC low-pass and high-pass networks (6dB/octave) can be recommended.

The results obtained are excellent because this is the best type of audio filter and the only one free from phase and transient distortion.

The rather poor out of band attenuation of single RC filters means that the loudspeaker must operate linearly well beyond the crossover frequency to avoid distortion.

A more effective solution, named "Active Power Filter" by SGS is shown in Figure 20.

Figure 20 : Active Power Filter



The proposed circuit can realize combined power amplifiers and 12dB/octave or 18dB/octave high-pass or low-pass filters.

In practice, at the input pins of the amplifier two equal and in-phase voltages are available, as required for the active filter operation.



The impedance at the pin (-) is of the order of  $100\Omega$ , while that of the pin (+) is very high, which is also what was wanted.

C1 = C2 = C3	R1	R2	R3
22 nF	8.2 kΩ	5.6 kΩ	33 kΩ

The component values calculated for  $f_{\rm c}$  = 900Hz using a Bessel 3rd order Sallen and Key structure are :

In the block diagram of Figure 21 is represented an active loudspeaker system completely realized using power integrated circuit, rather than the traditional discrete transistors on hybrids, very high quality is obtained by driving the audio spectrum into three bands using active crossovers (TDA2320A) and a separate amplifier and loud-speakers for each band.

A modern subwoofer/midrange/tweeter solution is used.

#### **PRATICAL CONSIDERATION**

#### Printed Circuit Board

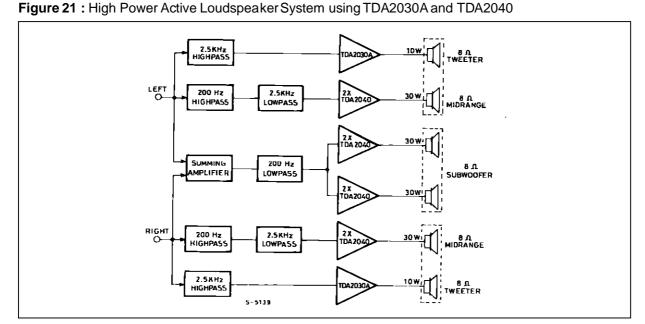
The layout shown in Figure 11 should be adopted by the designers. If different layouts are used, the ground points of input 1 and input 2 must be well decoupled from the gorund return of the output in which a high current flows.

# **Assembly Suggestion**

No electrical isolation is needed between the package and the heatsink with single supply voltage configuration.

# **Application Suggestions**

The recommended values of the components are those shown on application circuit of Fig. 10. Different values can be used. The following table can help the designer.



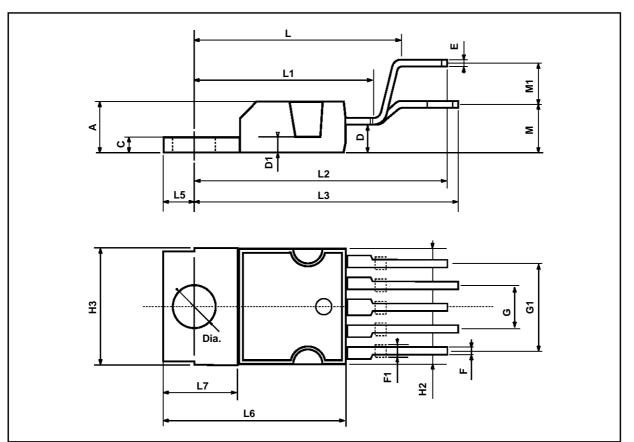
Comp.	Recom. Value	Purpose	Larger than Recommended Value	Smaller than Recommended Value	
R1	22kΩ	Non inverting input biasing	Increase of input impedance	Decrease of input impedance	
R2	680Ω	Closed loop gain setting	Decrease of gain (*)	Increase of gain	
R3	22kΩ	Closed loop gain setting	Increase of gain	Decrease of gain (*)	
R4	4.7Ω	Frequency stability	Danger of oscillation at high frequencies with inductive loads		
C1	1μF	Input DC decoupling		Increase of low frequencies cut-off	
C2	22µF	Inverting DC decoupling		Increase of low frequencies cut-off	
C3, C4	0.1µF	Supply voltage bypass		Danger of oscillation	
C5, C6	220µF	Supply voltage bypass		Danger of oscillation	
C7	0.1µF	Frequency stability		Danger of oscillation	

(\*) The value of closed loop gain must be higher than 24dB



DIM.	mm			inch			
DIN.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А			4.8			0.189	
С			1.37			0.054	
D	2.4		2.8	0.094		0.110	
D1	1.2		1.35	0.047		0.053	
E	0.35		0.55	0.014		0.022	
F	0.8		1.05	0.031		0.041	
F1	1		1.4	0.039		0.055	
G		3.4		0.126	0.134	0.142	
G1		6.8		0.260	0.268	0.276	
H2			10.4			0.409	
H3	10.05		10.4	0.396		0.409	
L		17.85			0.703		
L1		15.75			0.620		
L2		21.4			0.843		
L3		22.5			0.886		
L5	2.6		3	0.102		0.118	
L6	15.1		15.8	0.594		0.622	
L7	6		6.6	0.236		0.260	
М		4.5			0.177		
M1		4			0.157		
Dia	3.65		3.85	0.144		0.152	

#### PENTAWATT PACKAGE MECHANICAL DATA



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