











1.76

mW/°C



6-Pin DIP Optoisolators for Power Supply Applications (No Base Connection)

The MOC8101, MOC8102, MOC8103, MOC8104 and MOC8105 devices consist of a gallium arsenide LED optically coupled to a silicon phototransistor in a dual-in-line package.

- Closely Matched Current Transfer Ratio (CTR) Minimizes Unit-to-Unit Variation
- Narrow (CTR) Windows that translate to a Narrow and Predictable Open Loop Gain Window
- Very Low Coupled Capacitance along with No Chip to Pin 6 Base Connection for Minimum Noise Susceptibility
- To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.

Applications

- Switchmode Power Supplies (Feedback Control)
- AC Line/Digital Logic Isolation
- Interfacing and coupling systems of different potentials and impedances

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
INPUT LED			
Forward Current — Continuous	ΙF	60	mA
Forward Current — Peak (PW = 100 μs, 120 pps)	I _F (pk)	1	А
Reverse Voltage	٧R	6	Volts
LED Power Dissipation @ T _A = 25°C Derate above 25°C	PD	120 1.41	mW mW/°C
OUTPUT TRANSISTOR			•
Collector–Emitter Voltage	VCEO	30	Volts
Emitter-Collector Voltage	VECO	7	Volts
Collector Current — Continuous	IC	150	mA
Detector Power Dissipation @ T _A = 25°C	PD	150	mW

TOTAL DEVICE

Derate above 25°C

Input–Output Isolation Voltage(1) (f = 60 Hz, t = 1 sec.)	VISO	7500	Vac(pk)
Total Device Power Dissipation @ T _A = 25°C Derate above 25°C	PD	250 2.94	mW mW/°C
Ambient Operating Temperature Range ⁽²⁾	TA	-55 to +100	°C
Storage Temperature Range ⁽²⁾	T _{stg}	-55 to +150	°C
Lead Soldering Temperature (1/16" from case, 10 sec. duration)	TL	260	°C

- 1. Input-Output Isolation Voltage, VISO, is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4 and 5 are common.
- 2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

Preferred devices are Motorola recommended choices for future use and best overall value. GlobalOptoisolator is a trademark of Motorola, Inc.

REV₁

MOC8101 [CTR = 50-80%]

MOC8102 [CTR = 73-117%]

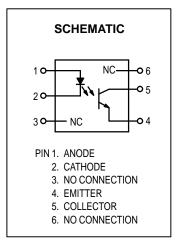
MOC8103 [CTR = 108-173%]

MOC8104

MOC8105

*Motorola Preferred Device

STYLE 3 PLASTIC STANDARD THRU HOLE **CASE 730A-04**



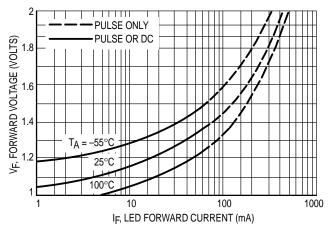
MOC8101 MOC8102 MOC8103 MOC8104 MOC8105

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)(1)

Characteristic		Symbol	Min	Typ ⁽¹⁾	Max	Unit
INPUT LED			•			
Forward Voltage (I _F = 10 mA)		٧F	1.0	1.15	1.5	V
Reverse Leakage Current (V _R = 5.0 V)	IR	_	0.05	10	μΑ	
Capacitance		С	_	18	_	pF
OUTPUT TRANSISTOR						
Collector–Emitter Dark Current (V	CE = 10 V, T _A = 25°C)	ICEO1	_	1.0	50	nA
(V	CE = 10 V, T _A = 100°C)	ICEO2	_	1.0	_	μΑ
Collector–Emitter Breakdown Voltage (I _C =	1.0 mA)	V(BR)CEO	30	45	_	V
Emitter-Collector Breakdown Voltage (I _E =	100 μΑ)	V(BR)ECO	7.0	7.8	_	V
Collector–Emitter Capacitance (f = 1.0 MHz	C _{CE}	_	7.0	_	pF	
COUPLED		•	•		•	
Output Collector Current (I _F = 10 mA, V _{CE} = 10 V)	MOC8101 MOC8102 MOC8103 MOC8104 MOC8105	I _C (CTR) ⁽²⁾	5.0 (50) 7.3 (73) 10.8 (108) 16 (160) 6.5 (65)	6.5 (65) 9.0 (90) 14 (140) 20 (200) 10 (100)	8.0 (80) 11.7 (117) 17.3 (173) 25.6 (256) 13.3 (133)	mA (%)
Collector–Emitter Saturation Voltage (I _C = 500 μA, I _F = 5.0 mA)		VCE(sat)	_	0.15	0.4	V
Turn–On Time (I _C = 2.0 mA, V _{CC} = 10 V, R _L = 100 Ω) ⁽³⁾		ton	_	7.5	20	μs
Turn–Off Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω) ⁽³⁾		t _{off}	_	5.7	20	μs
Rise Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω) ⁽³⁾		t _r	_	3.2	_	μs
Fall Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω) ⁽³⁾		t _f	_	4.7	_	μs
Isolation Voltage (f = 60 Hz, t = 1.0 sec.) ⁽⁴⁾		VISO	7500	_	_	Vac(pk)
Isolation Resistance (V _{I–O} = 500 V) ⁽⁴⁾		RISO	10 ¹¹	_	_	Ω
Isolation Capacitance (V _{I-O} = 0, f = 1.0 MHz) ⁽⁴⁾		C _{ISO}	_	0.2	_	pF

- 1. Always design to the specified minimum/maximum electrical limits (where applicable).
- 2. Current Transfer Ratio (CTR) = I_C/I_F x 100%.
- 3. For test circuit setup and waveforms, refer to Figure 7.
- 4. For this test, Pins 1 and 2 are common, and Pins 4 and 5 are common.

TYPICAL CHARACTERISTICS





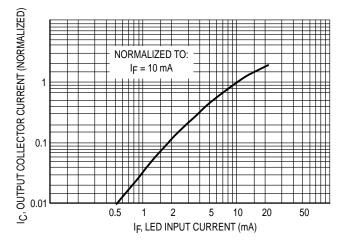


Figure 2. Output Current versus Input Current

MOC8101 MOC8102 MOC8103 MOC8104 MOC8105

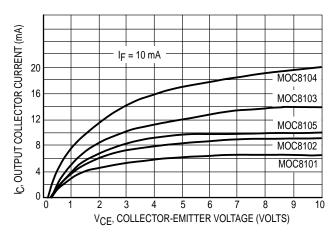


Figure 3. Collector Current versus Collector–Emitter Voltage

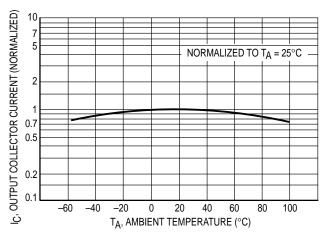


Figure 4. Output Current versus Ambient Temperature

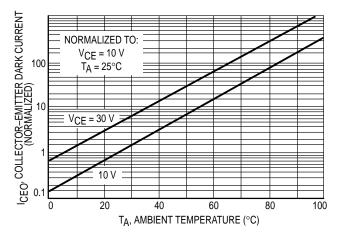


Figure 5. Dark Current versus Ambient Temperature

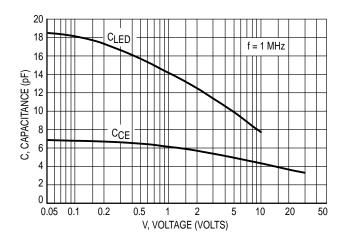


Figure 6. Capacitance versus Voltage

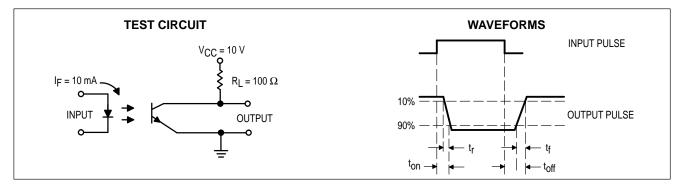
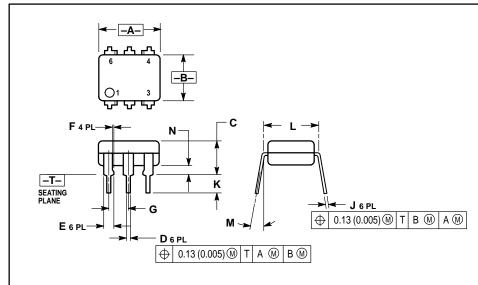


Figure 7. Switching Time Test Circuit and Waveforms

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MOC8101 MOC8102 MOC8103 MOC8104 MOC8105

PACKAGE DIMENSIONS



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION L TO CENTER OF LEAD WHEN
- FORMED PARALLEL.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.320	0.350	8.13	8.89
В	0.240	0.260	6.10	6.60
С	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
Е	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
J	0.008	0.012	0.21	0.30
K	0.100	0.150	2.54	3.81
L	0.300 BSC		7.62 BSC	
M	0°	15°	0°	15°
N	0.015	0.100	0.38	2.54

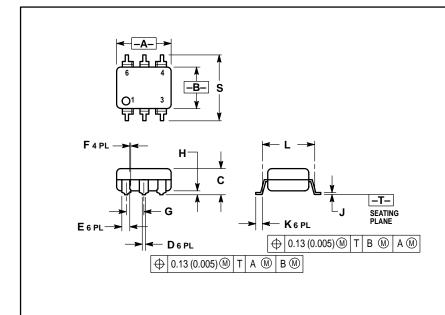
STYLE 3:

PIN 1. ANODE

- CATHODE NC
- EMITTER
- COLLECTOR NC
- 5. 6.

CASE 730A-04 ISSUE G

CASE 730C-04 **ISSUE D**



- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.320	0.350	8.13	8.89
В	0.240	0.260	6.10	6.60
С	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
E	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
Н	0.020	0.025	0.51	0.63
J	0.008	0.012	0.20	0.30
K	0.006	0.035	0.16	0.88
L	0.320	0.320 BSC 8.13 BSC		BSC
S	0.332	0.390	8.43	9.90

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