# **Dual Channel Small Outline Optoisolators**

# **Transistor Output**

The MOCD211 device consists of two gallium arsenide infrared emitting diodes optically coupled to two monolithic silicon phototransistor detectors, in a surface mountable, small outline, plastic package. It is ideally suited for high density applications and eliminates the need for through—the—board mounting.

- Dual Channel Coupler
- Convenient Plastic SOIC-8 Surface Mountable Package Style
- Minimum V(BR)CEO of 30 Volts Guaranteed
- Standard SOIC–8 Footprint, with 0.050" Lead Spacing
- · Shipped in Tape and Reel, which conforms to EIA Standard RS481A
- · Compatible with Dual Wave, Vapor Phase and IR Reflow Soldering
- High Input-Output Isolation of 3000 Vac (rms) Guaranteed
- Meets U.L. Regulatory Requirements, File #E54915

#### **Ordering Information:**

- To obtain MOCD211 in tape and reel, add R2 suffix to device number as follows:
   R2 = 2500 units on 13" reel
- To obtain MOCD211 in quantities of 50 (shipped in sleeves) no suffix

#### **Marking Information:**

MOCD211 = D211

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Value	Unit
ΙF	60	mA
I <sub>F</sub> (pk)	1.0	А
٧R	6.0	V
PD	90 0.8	mW mW/°C
VCEO	30	V
VCBO	70	V
VECO	7.0	V
IC	150	mA
PD	150 1.76	mW mW/°C
	IF IF(pk) VR PD  VCEO VCBO VECO IC	IF   60   1.0   VR   6.0   PD   90   0.8

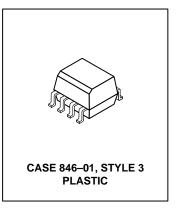
NOTE: Thickness through insulation between input and output is  $\geq 0.5$  mm.

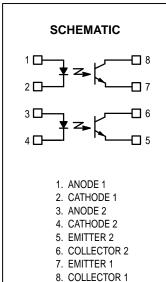
# **MOCD211**

[CTR = 20% Min]

Motorola Preferred Device

DUAL CHANNEL SMALL OUTLINE OPTOISOLATOR TRANSISTOR OUTPUT





Preferred devices are Motorola recommended choices for future use and best overall value.

## **MOCD211**

## **MAXIMUM RATINGS** — continued ( $T_A = 25^{\circ}C$ unless otherwise noted)

Rating

	9		<b>- J</b>			•
TOTAL DEVICE		•			•	
Input–Output Isolation Voltage(1,2) (60 Hz, 1.0 sec. duration)			VISO	3000		Vac(rms)
Total Device Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C			P <sub>D</sub>	250 2.94		mW mW/°C
Ambient Operating Temperature Range(3)			T <sub>A</sub>	-55 to +100		°C
Storage Temperature Range(3)			T <sub>stg</sub>	-55 to +150		°C
Lead Soldering Temperature (1/16" from case, 10 sec. duration)			_	260		°C
ELECTRICAL CHARACTERIST	CS (T <sub>A</sub> = 25°C unless otherwis	e noted)(4)				
Characteristic		Symbol	Min	Typ <sup>(4)</sup>	Max	Unit
NPUT LED						
Forward Voltage (I <sub>F</sub> = 1.0 mA)		٧F	_	1.15	1.5	V
Reverse Leakage Current (V <sub>R</sub> = 6.0	) V)	IR	_	0.1	100	μΑ
Capacitance		С		18	— pl	
OUTPUT TRANSISTOR			•			
Collector–Emitter Dark Current	$(V_{CE} = 5.0 \text{ V}, T_{A} = 25^{\circ}\text{C})$	ICEO1		1.0	50	nA
	$(V_{CE} = 5.0 \text{ V}, T_{A} = 100^{\circ}\text{C})$	ICEO2		1.0	_	μА
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 100 μA)		V(BR)CEO	30	90	_	V
Emitter–Collector Breakdown Voltage (I <sub>E</sub> = 100 μA)		V <sub>(BR)ECO</sub>	7.0	7.8	_	V
Collector–Emitter Capacitance (f = 1.0 MHz, V <sub>CE</sub> = 0)		C <sub>CE</sub>	T -	7.0	_	pF
COUPLED						
Output Collector Current (IF = 10 mA, V <sub>CE</sub> = 10 V)	MOCD211	I <sub>C</sub> (CTR) <sup>(5)</sup>	2.0 (20)	6.5 (65)	_	mA (%)
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 2.0 mA, I <sub>F</sub> = 1.0 mA)		V <sub>CE(sat)</sub>	_	0.15	0.4	V
Turn–On Time (I <sub>C</sub> = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ )		t <sub>on</sub>	_	7.5	_	μs
Turn–Off Time (I <sub>C</sub> = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ )		t <sub>off</sub>	_	5.7	_	μs
Rise Time (I <sub>C</sub> = 2.0 mA, $V_{CC}$ = 10 V, $R_L$ = 100 $\Omega$ )		t <sub>r</sub>	_	3.2	_	μs
Fall Time (I <sub>C</sub> = 2.0 mA, V <sub>CC</sub> = 10 V, R <sub>L</sub> = 100 $\Omega$ )		t <sub>f</sub>	T -	4.7	_	μѕ
Input-Output Isolation Voltage (f = 60 Hz, t = 1.0 sec.)(1,2)		VISO	3000	_	_	Vac(rms
Isolation Resistance (V <sub>I–O</sub> = 500 V) <sup>(2)</sup>		RISO	1011	<u> </u>	_	Ω
	(0)	<b>-</b>		<del></del>	<del>                                     </del>	

CISO

**Symbol** 

Value

Unit

- 1. Input-Output Isolation Voltage, VISO, is an internal device dielectric breakdown rating.
- 2. For this test, pins 1, 2, 3 and 4 are common, and pins 5, 6, 7 and 8 are common.
- 3. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.
- 4. Always design to the specified minimum/maximum electrical limits (where applicable).
- 5. Current Transfer Ratio (CTR) = I<sub>C</sub>/I<sub>F</sub> x 100%.

Isolation Capacitance  $(V_{I-O} = 0, f = 1.0 \text{ MHz})(2)$ 

0.2

pF

## **TYPICAL CHARACTERISTICS**

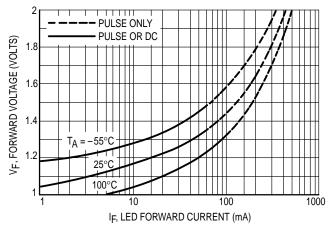


Figure 1. LED Forward Voltage versus Forward Current

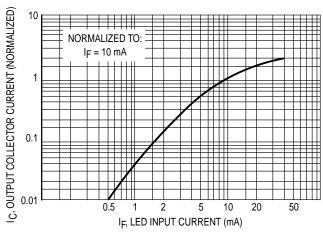


Figure 2. Output Current versus Input Current

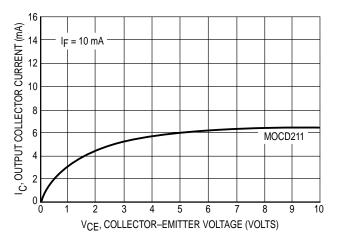


Figure 3. Output Current versus Collector–Emitter Voltage

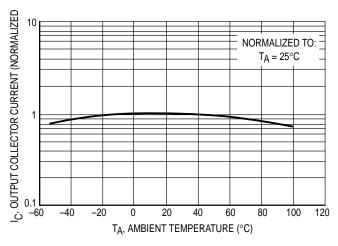


Figure 4. Output Current versus Ambient Temperature

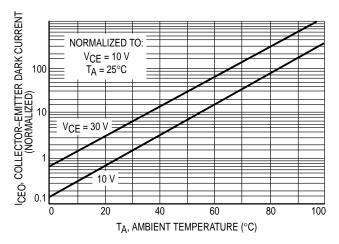


Figure 5. Dark Current versus Ambient Temperature

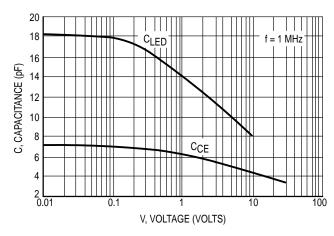
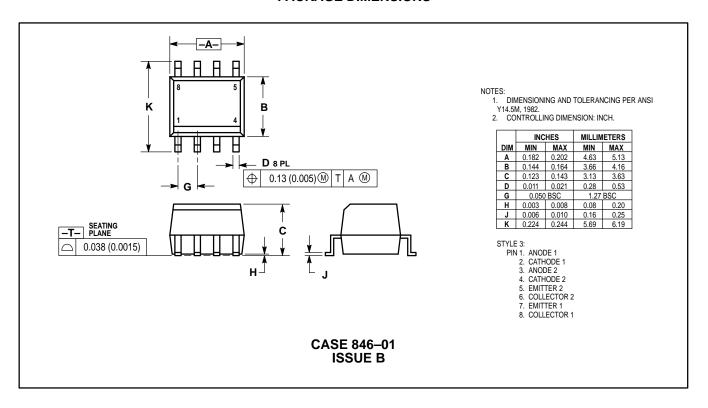


Figure 6. Capacitance versus Voltage

#### PACKAGE DIMENSIONS



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