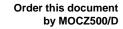
MOTOROLA SEMICONDUCTOR TECHNICAL DATA



MOCZ500

Motorola Preferred Device

OPTOISOLATOR

ZERO CROSS

TRIAC OUTPUT

CASE 730J-01



Mini Zero-Crossing AC SSR

This device consists of a gallium arsenide infrared emitting diode optically coupled to a zero–cross triac circuit and a power triac. It is capable of driving loads up to 500 mA rms on AC voltages from 20 to 280 V rms.

- Provides Normally Open AC Output with 500 mA Rating @ 40°C
- Small Outline, Standard 6-PIN DIP Package
- Simplified Logic Control of 240 Vac Power
- High Input–Output Isolation of 7500 Vac (rms)
- 7 Amp Single Cycle Surge Capability
- Wide Load Power Factor Range 0.1-1
- Low Input/Output Capacitance

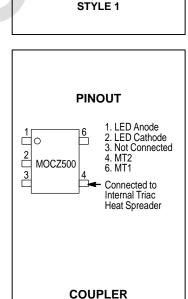
Applications:

- Logic to AC Line Interface
- Microprocessor to AC Line Peripheral
- Industrial Controls
- EM Relays and Contactors
- Small AC Motor Drives
- Incandescent Lamp Drive

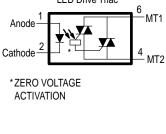
- Appliance Solenoids
- Appliance Actuators
- Appliance Fan Motors
 - Appliance Lights

MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)

Rating	Rating Symbol Value		Unit
INPUT LED			
Reverse Voltage	VR	6	V
Forward Current — Continuous	١ _F	50	mA
OUTPUT TRIAC		-	
Off-State Output Terminal Voltage (1)	VDRM	600	V
Peak Repetitive Surge Current (1 Cycle)	ITSM	7	А
Main Terminal Fusing Current (t = 8.3 ms)	I ² T	0.4	A ² sec
On–State Current Range	I _T (rms)	0.030 to 0.500	А
Load Power Factor Range	pF	0.1 – 1.0	—
TOTAL DEVICE			
Isolation Surge Voltage (2)	VISO	7500	Vac(pk)
Total Power Dissipation @ T _A = 40°C (Device Soldered on PCB)	PD	600	mW
Junction Temperature Range	Тј	-40 to +125	°C
Ambient Operating Temperature Range	ТА	-40 to +85	°C
Ambient Operating Relative Humidity @ T _A = 85°C	RHA	85	%
Storage Temperature Range	T _{stg}	-40 to +125	°C
Soldering Temperature (10 sec)	ТL	260	°C







1. Test voltages must be applied within dv/dt rating.

2. Input–Output isolation voltage, V_{ISO}. is an internal device dielectric breakdown rating. For this test, pins 1 and 2 are common and pins 4 and 6 are common.

Preferred devices are Motorola recommended choices for future use and best overall value.



THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Air (Device Soldered on PCB)	$R_{ heta JA}$	130	°C/W
Thermal Resistance, Junction to Case (Pin 4) (Device Soldered on PCB)	R _{θJC}	40	°C/W

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
INPUT LED	·				•
Reverse Leakage Current (V _R = 6 V)	IR	—	0.05	100	μA
Forward Voltage (I _F = 10 mA)	VF	—	1.2	1.5	V
OUTPUT TRIAC					
Leakage with LED Off @ $T_A = 85^{\circ}C$ (V _{DRM} = 600 V)	IDRM	_	—	100	μA
Critical Rate of Rise of Off–State Voltage (Static) (1) (Vp = 400 V)	dv/dt(s)	_	2,000	_	V/µs
LED On, Driver Holding Current	lH1	—	150	500	μΑ
COUPLED					
LED Trigger Current Required to Latch Output (2) (3) (Main Terminal Voltage = 5 V)	lFT(on)	-	_	10	mA
On-State Voltage (I _T = 500 mA)	VTM	—	1.2	1.5	V
Inhibit Voltage (I _F = I _{FT})	VINH	—	10	20	V
Commutating dv/dt	dv/dt (c)	10	_	_	V/µs
Common–Mode Input–Output dv/dt	dv/dt (cm)	40,000	_	_	V/µs
Input–Output Capacitance	CISO	—	—	1	pF
Isolation Resistance @ 500 Vdc	RISO	10 ¹²		—	Ohms

1. Additional dv/dt information, including test methods, can be found in Motorola applications note AN1048/D, Figure 40.

2. All devices are guaranteed to trigger at as I_F value less than or equal to the max I_{FT}. Therefore, the recommended operating I_F lies between the device's maximum I_{FT}(on) limit and the Maximum Rating of 60 mA.

3. Current-limiting resistor required in series with LED.

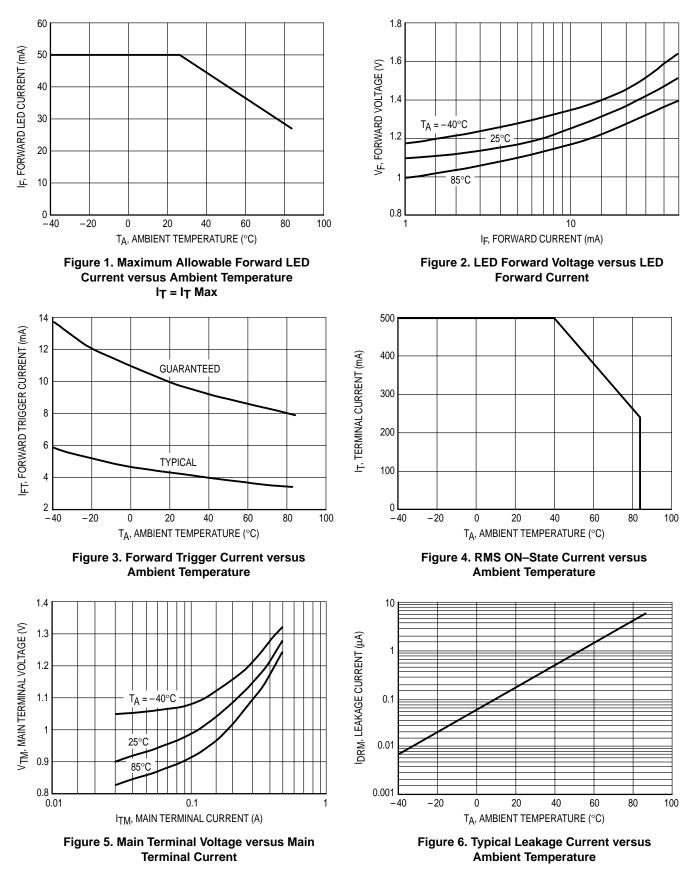
ENVIRONMENTAL TEST REQUIREMENTS

Test	Test Conditions	
Autoclave	T _A = 121°C, RH = 100%, P = 15 PSIG, 48 Hr.	
Moisture Resistance	Mil–Std–883, Method 1004	
Temp Cycle	$T_A = -40/+125^{\circ}C$, Air to Air, Dwell ≥ 15 min., Transfer ≤ 5 min., 200 Cycles	
Resistance to Solder Heat	Mil–Std–750, Method 2031, 260°C followed by VISO	
Lead Pull	Mil–Std–750, Method 2036, Condition A, 2 lbs., 1 min.	

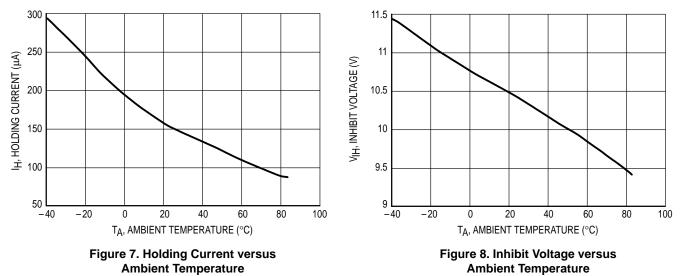
LIFE TEST REQUIREMENTS

	Test Conditions		
Test	Environment	Bias	Duration
High Temperature, Reverse Bias	T _A = +100°C	V _{TM} = 280 Vac	1000 Hr.
High Humidity, High Temperature, Reverse Bias	T _A = +85°C RH = 85%	V _{TM} = 100 Vdc Pin 4 = + Pin 6 = -	500 Hr.
Intermittent Operating Life	$t_{OP} = 2 \text{ min.}$ $t_{Off} = 2 \text{ min.}$ $T_A = +25^{\circ}C$	I _F = 50 mA ITM = 60 mA	1000 Hr.
ESD	Human Body Model & Machine Models 1 & 2	N/A	N/A

TYPICAL ELECTRICAL CHARACTERISTICS



TYPICAL ELECTRICAL CHARACTERISTICS (continued)



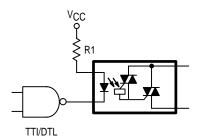
APPLICATION CONSIDERATIONS

Input Drive Circuit

The MOCZ500 SSR is guaranteed to trigger with an input current of 10 mA at 25°C. This trigger current increases with lower ambient temperatures as shown on Figure 3 Forward Trigger Current (I_{FT}) versus Ambient Temperature.

When the input drive circuit is capable to supply the MOCZ500 input LED trigger current, only a current limiting resistor in series with the LED is required. TTL, DTL and microcontrollers with enhanced current capability output ports are able to meet this requirement.

Most CMOS logic circuits and Microcontroller output ports are not rated to sink or source currents required to trigger the MOCZ500. In this case a drive circuit is required as shown in Figure 10 or a TTL buffer interface circuit as shown in Figure 9.



R1 for V_{CC} (low) 4.5 V, I_{FT} = 10 mA, V_F LED = 1.2 V, V_{OL} = 0.5 V R1 = (4.5 V - 1.2 V - 0.5 V): 10 mA = 280 Ω

Choose 270 Ω

Figure 9. Input Drive Circuit

Snubber Circuit

Snubberless operation of resistive loads is possible, but snubbers are recommended for all applications. A typical application is shown in Figure 11. The snubber attenuates the high kickback voltages and commutating dv/dt generated by inductive loads during the turn off of the SSR. It also protects the SSR from line transients generated elsewhere within the equipment (for example inductive loads switched by mechanical contacts such as relays manual on/off switches etc.) or outside the equipment such as air conditioners, electrical heaters and motors.

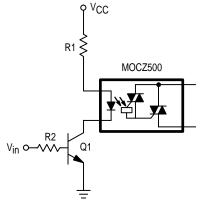
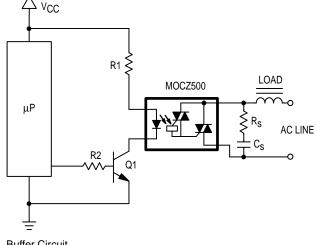


Figure 10. Noninverting Discrete NPN Buffer and Level Shifter



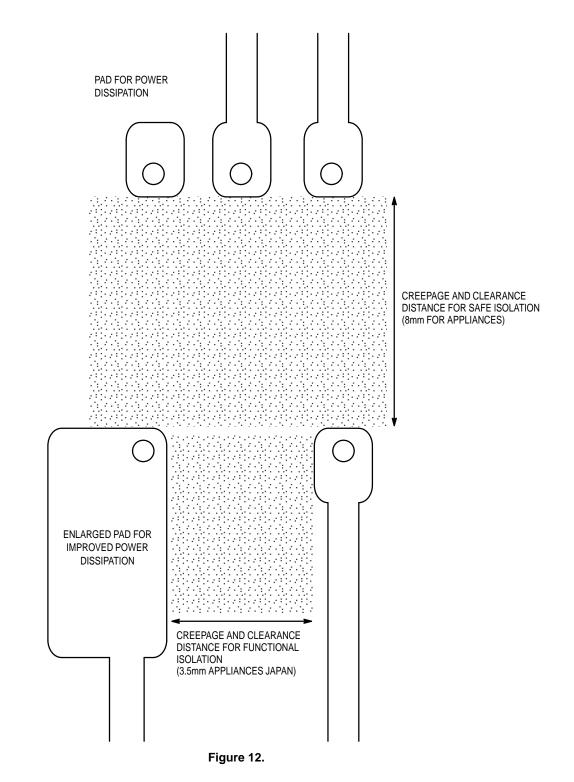
Buffer Circuit R1 = ($V_{CC} - V_{FLED} - V_{sat} Q1$): IF MOCZ500 R2 = 10 k Ω Q1 = General Purpose Trans. NPN Typical Snubber circuit: For inductive and resistive loads R_S = 45 Ω C_S = 0.01 μ F

Figure 11. Typical Application with an μ P Output Buffer

Snubbers are also necessary to pass noise immunity tests such as IEC1000 4–4 for fast transients. In this test fast rising high voltage spikes are superimposed onto the line voltage to simulate AC line transients.

Switching Loads with Currents Below the Minimum Current Rating

The MOCZ500 is capable to switch any inductive or resistive load within its rating of minimum 30 mA and a maximum of 500 mA RMS. At operating currents below the minimum specified value the Power triac remains in the off state and the triac driver carries the current. This may cause a problem, because the triac driver has a significant lower commutating dv/dt than the power triac. For loads below 30 mA AC rms a snubber is mandatory. Evaluations with various low current inductive and resistive loads concluded that a snubber of R = 100 Ω and C = 10 nF is sufficient.



Thermal Considerations

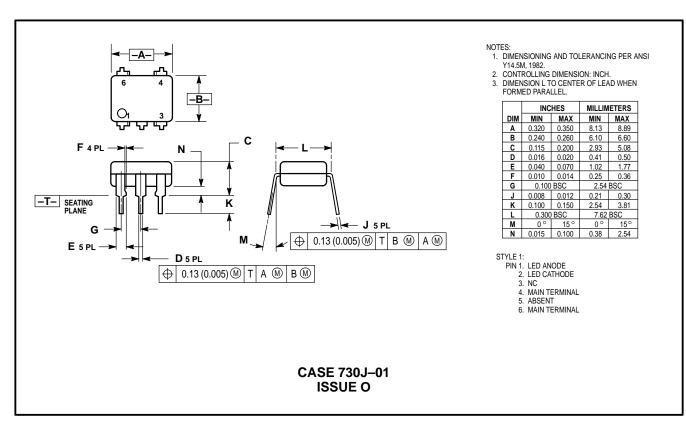
Heat generated inside the MOCZ500's power triac is transferred through the leads to the circuit board where it is dissipated. It is therefore important to solder all leads to the circuit board. Pin 4 is thermally and electrically direct connected to the Power triac and carries the highest amount of thermal energy. For loads which approach the maximum current rating of the SSR it is advisable to layout the pad size for pin 4 as large as possible. See Figure 12 which considers thermal and Regulatory requirements on a PCB.

Regulatory Safety Considerations

The MOCZ500 is designed to meet the National and International Regulatory requirements for safe isolation

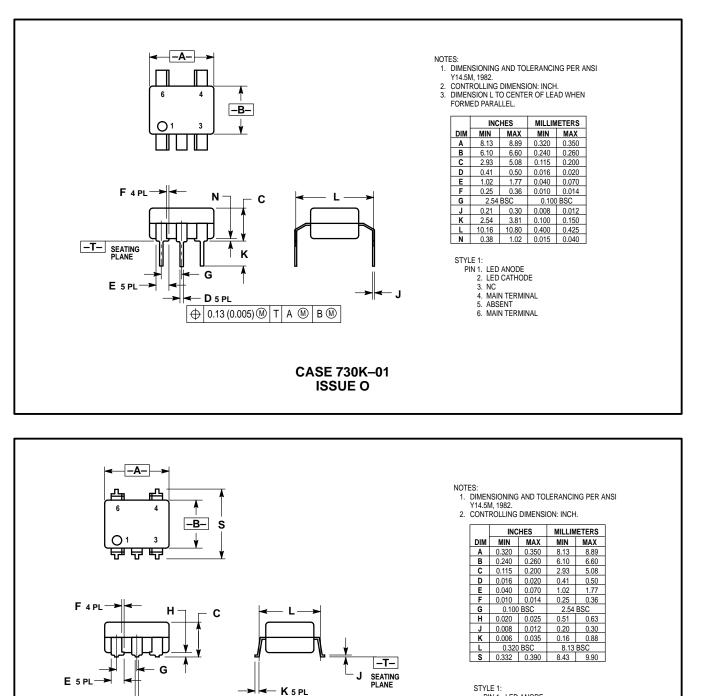
between input and output and functional isolation creepage and clearance distances between the AC output pins.

Many equipment standards demand a creepage and clearance distance between input and output circuit of 8mm and a thickness through insulation of 0.4mm (16 mil). All Motorola Optocouplers do meet the thickness through insulation requirement. Product with lead bend option "T" meets the creepage path requirement. The most stringent requirement for creepage and clearance between the AC output pins is 3.5mm. Figure 12 shows a PCB pattern layout which meets the regulatory requirements for 115 Vrms and 240 Vrms supply line applications.



OUTLINE DIMENSIONS

OUTLINE DIMENSIONS (continued)



⊕ 0.13 (0.005) M T B M A M

CASE 730L-01 ISSUE A

Motorola Optoelectronics Device Data

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| ⊕ | 0.13 (0.005) (M) | T | A (M) | B (M)

PIN 1. LED ANODE 2. LED CATHODE

3. NC 4. MAIN TERMINAL

5. ABSENT 6. MAIN TERMINAL

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