MTIL113

STYLE 1 PLASTIC

STANDARD THRU HOLE



6-Pin DIP Optoisolators Darlington Output

The MTIL113 device consists of a gallium arsenide infrared emitting diode optically coupled to a monolithic silicon photodarlington detector.

This device is designed for use in applications requiring high collector output currents at lower input currents.

- Higher Sensitivity to Low Input Drive Current
- Meets or Exceeds All JEDEC Registered Specifications

Applications

- Low Power Logic Circuits
- · Interfacing and coupling systems of different potentials and impedances
- Telecommunications Equipment
- Portable Electronics
- Solid State Relays

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Symbol	Value	Unit
VR	3	Volts
lF	60	mA
PD	100 1.41	mW mW/°C
	V _R IF	V _R 3 I _F 60 P _D 100

6. BASE

OUTPUT DETECTOR

Collector-Emitter Voltage	VCEO	30	Volts
Emitter–Collector Voltage	VECO	5	Volts
Collector-Base Voltage	VCBO	30	Volts
Collector Current — Continuous	ΙC	125	mA
Detector Power Dissipation @ T _A = 25°C Derate above 25°C	PD	150 1.76	mW mW/°C

TOTAL DEVICE

Isolation Surge Voltage ⁽²⁾ (Peak ac Voltage, 60 Hz, 1 sec Duration)	VISO	7500	Vac(pk)
Total Device Power Dissipation @ T _A = 25°C Derate above 25°C	PD	250 2.94	mW mW/°C
Ambient Operating Temperature Range ⁽³⁾	TA	-55 to +100	°C
Storage Temperature Range ⁽³⁾	T _{stg}	-55 to +150	°C
Soldering Temperature (10 sec, 1/16" from case)	тլ	260	°C

1. All Motorola 6-Pin devices exceed JEDEC specification and are 7500 Vac(pk).

2. Isolation surge voltage is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.

3. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)⁽¹⁾

Characteristic	Symbol	Min	Typ (1)	Max	Unit
INPUT LED	•				
Reverse Leakage Current (V _R = 3 V, R _L = 1 M ohms)	IR	—	0.05	100	μA
Forward Voltage (I _F = 10 mA)	VF	—	1.34	1.5	Volts
Capacitance ($V_R = 0 V$, f = 1 MHz)	С	—	1.8	_	pF
OUTPUT DETECTOR ($T_A = 25^{\circ}C$ and $I_F = 0$, unless otherwise no	oted)	-	-		
Collector–Emitter Dark Current (V _{CE} = 10 V, Base Open)	ICEO	_	—	100	nA
Collector–Base Breakdown Voltage $(I_C = 100 \ \mu\text{A}, I_E = 0)$	V(BR)CBO	30	—	—	Volts
Collector–Emitter Breakdown Voltage $(I_C = 100 \ \mu\text{A}, I_B = 0)$	V(BR)CEO	30	—	—	Volts
Emitter–Collector Breakdown Voltage $(I_E = 100 \ \mu\text{A}, I_B = 0)$	V _{(BR)ECO}	5	—	—	Volts
DC Current Gain ($V_{CE} = 5 \text{ V}, I_C = 500 \mu \text{A}$)	hFE	—	16K	—	—
COUPLED (T _A = 25°C unless otherwise noted)	•				
Collector Output Current (3) (V _{CE} = 1 V, I _F = 10 mA)	I _C (CTR) ⁽²⁾	30 (300)	—	_	mA (%)
Isolation Surge Voltage(4,5) (60 Hz ac Peak, 1 Second)	VISO	7500	—	—	Vac(pk)
Isolation Resistance ⁽⁴⁾ (V = 500 V)	RISO	_	10 ¹¹	—	Ohms
Collector–Emitter Saturation Voltage(3) ($I_C = 2 \text{ mA}, I_F = 8 \text{ mA}$)	V _{CE(sat)}	_	—	1.25	Volts
Isolation Capacitance ⁽⁴⁾ (V = 0 V, f = 1 MHz)	C _{ISO}	_	0.2	—	pF
Turn–On Time ⁽⁶⁾ (I _C = 50 mA, I _F = 200 mA, V _{CC} = 10 V)	ton	_	0.6	5	μs
Turn–Off Time ⁽⁶⁾ (I _C = 50 mA, I _F = 200 mA, V _{CC} = 10 V)	toff	-	45	100	μs

1. Always design to the specified minimum/maximum electrical limits (where applicable).

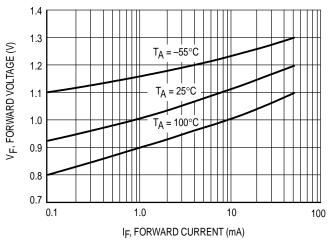
2. Current Transfer Ratio (CTR) = $I_C/I_F \times 100\%$.

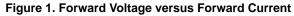
3. Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \leq 2%.

4. For this test, Pins 1 and 2 are common and Pins 4, 5 and 6 are common.

5. Isolation Surge Voltage, V_{ISO} , is an internal device dielectric breakdown rating.

6. For test circuit setup and waveforms, refer to Figures 8 and 9.





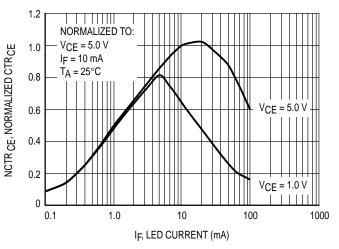
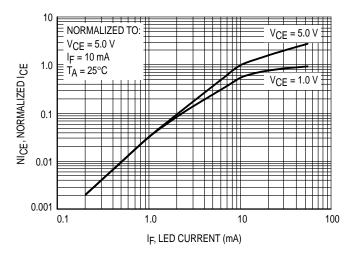


Figure 2. Normalized Non–Saturated and Saturated CTRce versus LED Current

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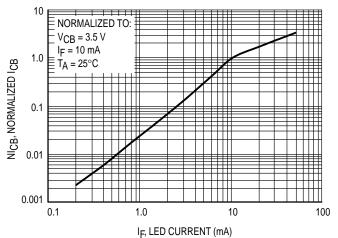


Figure 3. Normalized Non–Saturated and Saturated Collector–Emitter Current versus LED Current

Figure 4. Normalized Collector–Base Photocurrent versus LED Current

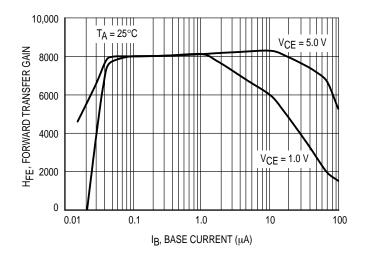


Figure 5. Non–Saturated and Saturated HFE versus Base Current

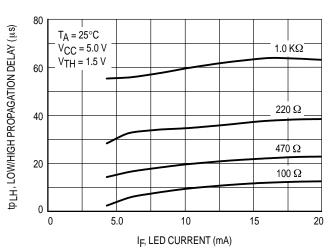


Figure 6. Low to High Propagation Delay versus Collector Load Resistance and LED Current

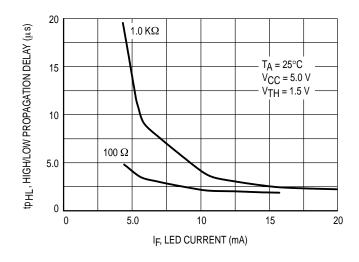
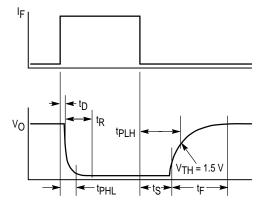


Figure 7. High to Low Propagation Delay versus Collector Load Resistance and LED Current



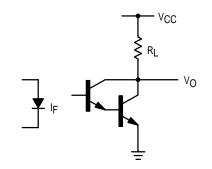
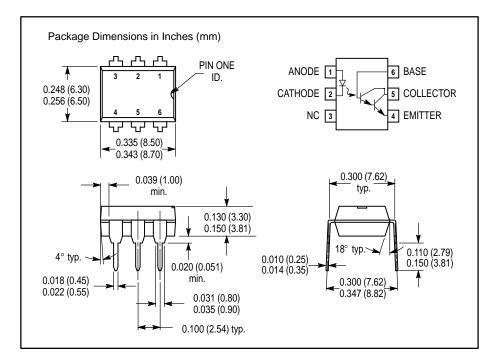


Figure 8. Switching Waveform

Figure 9. Switching Schematic



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