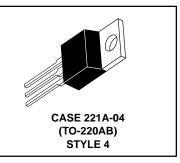
Triacs Silicon Bidirectional Thyristors

... designed for full-wave ac control applications primarily in industrial environments needing noise immunity.

- Guaranteed High Commutation Voltage dv/dt — 500 V/µs Min @ T_C = 25°C
- High Blocking Voltage V_{DRM} to 800 V
- Photo Glass Passivated Junction for Improved Power Cycling Capability and Reliability



TRIACs 20 AMPERES RMS 200 thru 800 VOLTS



-**O** MT1

d_G

MAXIMUM RATINGS (T_C = 25° C unless otherwise noted.)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage ⁽¹⁾ (T _J = -40 to +125°C, 1/2 Sine Wave 50 to 60 Hz, Open Gate)	V _{DRM}		Volts
MAC321-4 MAC321-6 MAC321-8 MAC321-10		200 400 600 800	
Peak Gate Voltage	V _{GM}	10	Volts
On-State Current RMS (T _C = +75°C Full Cycle Sine Wave 50 to 60 Hz)	^I T(RMS)	20	Amp
Peak Surge Current (One Full Cycle, 60 Hz, T _C = +75°C preceded and followed by Rated Current)	ITSM	150	Amp
Circuit Fusing Considerations (t = 8.3 ms)	l ² t	93	A ² s
Peak Gate Power (T _C = +75°C, Pulse Width = 2.0 μ s)	PGM	20	Watts
Average Gate Power (T _C = +75°C, t = 8.3 ms)	PG(AV)	0.5	Watt
Peak Gate Current	I _{GM}	2.0	Amp
Operating Junction Temperature Range	Тј	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

MT2 O

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC}	1.8	°C/W

1. V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

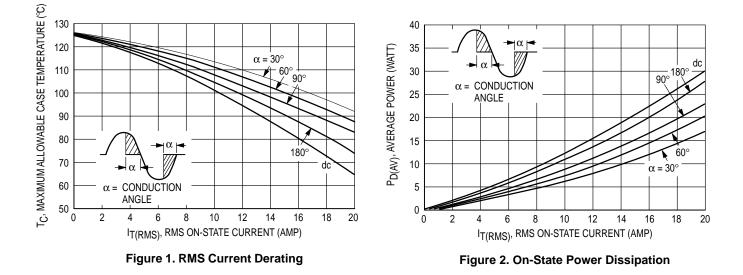


MAC321 Series

ELECTRICAL CHARACTERISTICS (T_C = 25° C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current (V _D = Rated V _{DRM} , Gate Open)	IDRM			10	
TJ = 25°C TJ = +125°C		_	_	10 2.0	μA mA
Peak On-State Voltage (Either Direction) (I_{TM} = 28 A Peak; Pulse Width \leq 2.0 ms, Duty Cycle \leq 2.0%)	VTM	—	1.4	1.7	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _L = 100 Ohms)	IGT				mA
MT2(+), G(+)		—	—	100	
MT2(+), G(-)		-	—	100	
MT2(–), G(–)		-	-	100	
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _I = 100 Ohms)	V _{GT}				Volts
MT2(+), G(+)		-	—	2.0	
MT2(+), G(–)		—	—	2.0	
MT2(–), G(–) (Main Terminal Voltage = Rated V _{DRM} , R _I = 10 k Ω , T _J = +125°C)		-	-	2.0	
MT2(+), G(+); MT2(–), G(–); MT2(+), G(–)		0.2	—	—	
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 200 mA)	Ч	—	_	100	mA
Turn-On Time (V _D = Rated V _{DRM} , I _{TM} = 28 A, I _{GT} = 120 mA, Rise Time = 0.1 μ s, Pulse Width = 2.0 μ s)	tgt	_	1.5	—	μs
Critical Rate of Rise of Off-State Voltage (V _D = Rated V _{DRM} , Exponential Voltage Rise, Gate Open)	dv/dt(s)				V/µs
TJ = 25°C TJ = +125°C		500 200	— —	_	

TYPICAL CHARACTERISTICS



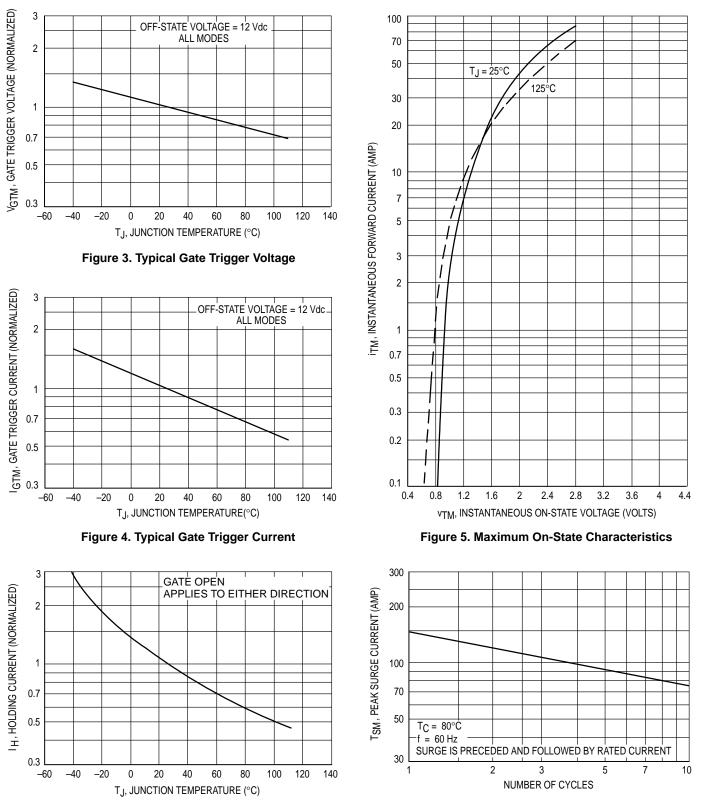


Figure 6. Typical Holding Current

Figure 7. Maximum On-Repetitive Surge Current

MAC321 Series

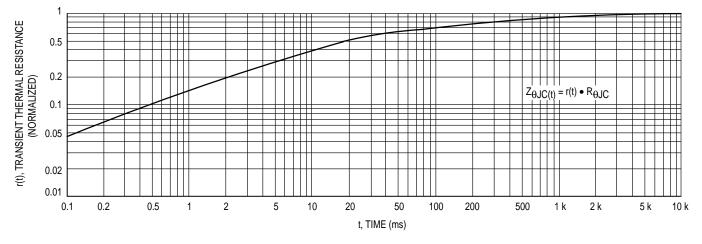
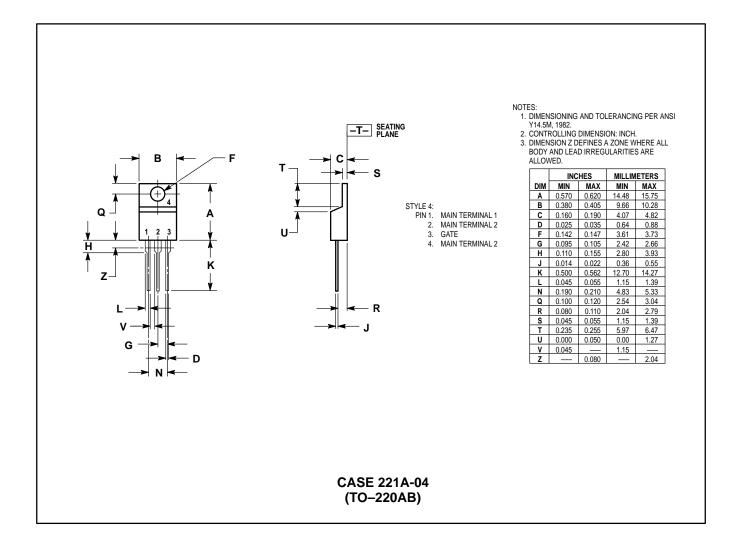


Figure 8. Thermal Response

PACKAGE DIMENSIONS



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