Thyristors

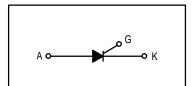
Silicon Controlled Rectifiers

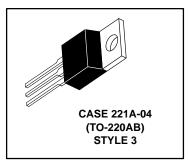
... designed for inverse parallel SCR output devices for solid state relays, welders, battery chargers, motor controls or applications requiring high surge operation.

- Photo Glass Passivated Blocking Junctions for High Temperature Stability, Center Gate for Uniform Parameters
- 550 Amperes Surge Capability
- · Blocking Voltage to 800 Volts

MCR265-2 thru MCR265-10

SCRs 55 AMPERES RMS 50 thru 800 VOLTS





MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted.)

| Rating | Symbol | Value | Unit |
|--|--------------------------|--------------------------------|-------|
| Peak Repetitive Forward and Reverse Blocking Voltage ⁽¹⁾ (T _J = 25 to 125°C, Gate Open) MCR265-2 MCR265-4 MCR265-6 MCR265-8 MCR265-10 | V _{DRM} VRRM | 50 200 400 600 800 | Volts |
| Forward Current (T _C = 70°C) (All Conduction Angles) | IT(RMS) IT(AV) | 55 35 | Amps |
| Peak Non-repetitive Surge Current — 8.3 ms (1/2 Cycle, Sine Wave) | I _{TSM} | 550 | Amps |
| Forward Peak Gate Power | PGM | 20 | Watts |
| Forward Average Gate Power | P _{G(AV)} | 0.5 | Watt |
| Forward Peak Gate Current (300 μs, 120 PPS) | I _{GM} | 2 | Amps |
| Operating Junction Temperature Range | TJ | -40 to +125 | °C |
| Storage Temperature Range | T _{stg} | -40 to +150 | °C |

^{1.} V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

These devices are rated for use in applications subject to high surge conditions. Care must be taken to insure proper heat sinking when the device is to be used at high sustained currents.



MCR265-2 thru MCR265-10

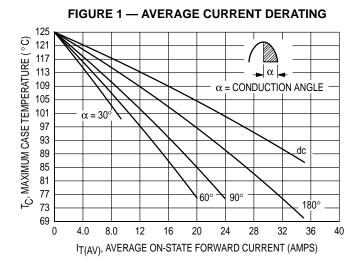
THERMAL CHARACTERISTICS

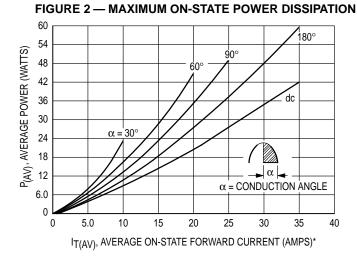
| Characteristic | Symbol | Max | Unit |
|---|----------------|-----|------|
| Thermal Resistance, Junction to Case | $R_{	heta JC}$ | 0.9 | °C/W |
| Thermal Resistance, Junction to Ambient | $R_{	heta JA}$ | 60 | °C/W |

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|-----------------|--------|----------|----------|----------|
| Peak Forward or Reverse Blocking Current (VAK = Rated VDRM or VRRM, Gate Open) TJ = 25°C TJ = 125°C | IDRM, IRRM | _ | _ | 10 2 | μA mA |
| Forward "On" Voltage ⁽¹⁾ (I _{TM} = 110 A) | VTM | _ | 1.5 | 1.9 | Volts |
| Gate Trigger Current (Continuous dc) (Anode Voltage = 12 Vdc, R _L = 100 Ohms) (T _C = -40°C) | lGT . | _ _ | 20 40 | 50 90 | mA |
| Gate Trigger Voltage (Continuous dc) (Anode Voltage = 12 Vdc, R _L = 100 Ohms) | VGT | _ | 1 | 1.5 | Volts |
| Gate Non-Trigger Voltage (Anode Voltage = Rated V _{DRM} , R _L = 100 Ohms, T _J = 125°C) | V _{GD} | 0.2 | _ | _ | Volts |
| Holding Current (Anode Voltage = 12 Vdc, Gate Open) | Ιн | _ | 30 | 75 | mA |
| Turn-On Time (I _{TM} = 55 A, I _{GT} = 200 mAdc) | ^t gt | _ | 1.5 | _ | μs |
| Critical Rate-of-Rise of Off-State Voltage (Gate Open, V_D = Rated V_{DRM} , Exponential Waveform) | dv/dt | _ | 50 | _ | V/µs |

^{1.} Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.





MCR265-2 thru MCR265-10

FIGURE 3 — GATE TRIGGER CURRENT

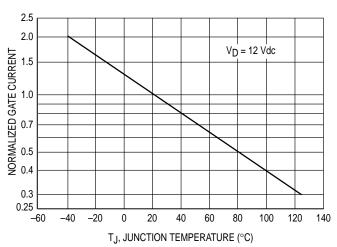


FIGURE 4 — GATE TRIGGER VOLTAGE

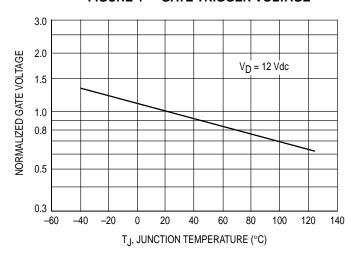


FIGURE 5 — HOLDING CURRENT

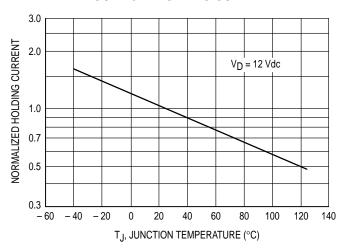


FIGURE 6 — TYPICAL ON-STATE CHARACTERISTICS

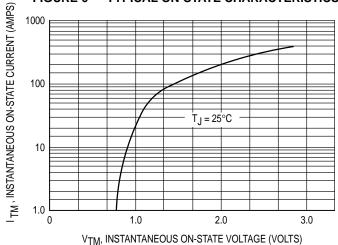
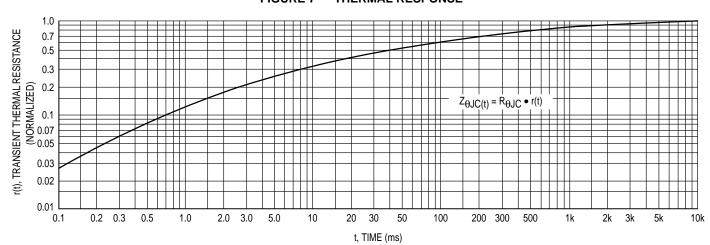
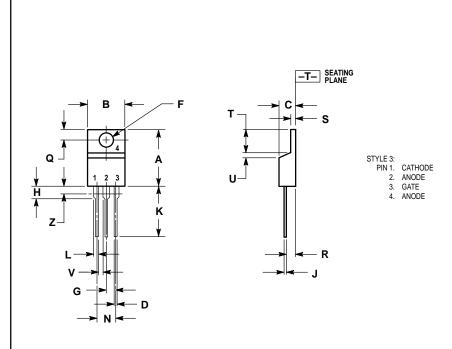


FIGURE 7 — THERMAL RESPONSE



PACKAGE DIMENSIONS



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

| | INCHES MILLIMET | | | IETERS | |
|-----|-----------------|-------|-------|--------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.570 | 0.620 | 14.48 | 15.75 | |
| В | 0.380 | 0.405 | 9.66 | 10.28 | |
| С | 0.160 | 0.190 | 4.07 | 4.82 | |
| D | 0.025 | 0.035 | 0.64 | 0.88 | |
| F | 0.142 | 0.147 | 3.61 | 3.73 | |
| G | 0.095 | 0.105 | 2.42 | 2.66 | |
| Н | 0.110 | 0.155 | 2.80 | 3.93 | |
| J | 0.014 | 0.022 | 0.36 | 0.55 | |
| K | 0.500 | 0.562 | 12.70 | 14.27 | |
| L | 0.045 | 0.055 | 1.15 | 1.39 | |
| N | 0.190 | 0.210 | 4.83 | 5.33 | |
| Q | 0.100 | 0.120 | 2.54 | 3.04 | |
| R | 0.080 | 0.110 | 2.04 | 2.79 | |
| S | 0.045 | 0.055 | 1.15 | 1.39 | |
| Т | 0.235 | 0.255 | 5.97 | 6.47 | |
| U | 0.000 | 0.050 | 0.00 | 1.27 | |
| ٧ | 0.045 | | 1.15 | | |
| Z | | 0.080 | | 2.04 | |

CASE 221A-04 (TO-220AB)

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

Literature Distribution Centers:

USA: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036.

EUROPE: Motorola Ltd.; European Literature Centre; 88 Tanners Drive, Blakelands, Milton Keynes, MK14 5BP, England.

JAPAN: Nippon Motorola Ltd.; 4-32-1, Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan.

ASIA PACIFIC: Motorola Semiconductors H.K. Ltd.; Silicon Harbour Center, No. 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong.

