Silicon Controlled Rectifiers

Reverse Blocking Triode Thyristors

. . . designed for industrial and consumer applications such as temperature, light and speed control; process and remote controls; warning systems; capacitive discharge circuits and MPU interface.

- Center Gate Geometry for Uniform Current Density
- All Diffused and Glass-Passivated Junctions for Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Low Trigger Currents, 200 μA Maximum for Direct Driving from Integrated Circuits

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit	
Peak Repetitive Forward and Reverse Blocking Voltage(1) $(T_J = -40 \text{ to } 110^{\circ}\text{C}) \\ (1/2 \text{ Sine Wave, R}_{GK} = 1 \text{ k}\Omega) \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	VDRM or VRRM	50 100 200 400 600 800	Volts	
On-State RMS Current (T _C = 75°C)	I _T (RMS)	10	Amps	
Peak Non-repetitive Surge Current (1/2 Cycle, 60 Hz, T _J = -40 to 110°C)	ITSM	100	Amps	
Circuit Fusing (t = 8.3 ms)	I ² t	40	A ² s	
Peak Gate Voltage (t ≤ 10 μs)	V _{GM}	±5	Volts	
Peak Gate Current (t ≤ 10 μs)	I _{GM}	1	Amp	
Peak Gate Power (t ≤ 10 μs)	PGM	5	Watts	
Average Gate Power	P _G (AV)	0.75	Watt	
Operating Junction Temperature Range	TJ	-40 to +110	°C	
Storage Temperature Range	T _{stg}	-40 to +150	°C	
Mounting Torque	_	8	inlb.	

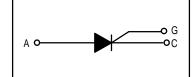
THERMAL CHARACTERISTICS

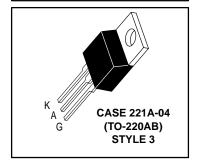
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.2	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	60	°C/W

1. VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

MCR310 Series

SCRs 10 AMPERES RMS 50 thru 800 VOLTS







MCR310 Series

ELECTRICAL CHARACTERISTICS (T_C = 25°C, R_{GK} = 1 k Ω unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Forward Blocking Current ⁽¹⁾ $T_C = 110^{\circ}C$ $(T_J = 110^{\circ}C, V_D = Rated V_{DRM})$ $T_C = 25^{\circ}C$	IDRM	_ _	_ _	500 10	μΑ μΑ
Peak Reverse Blocking Current ⁽¹⁾ $T_C = 110^{\circ}C$ $(T_J = 110^{\circ}C, V_R = Rated V_{RRM})$ $T_C = 25^{\circ}C$	IRRM	_ _	_ _	500 10	μΑ μΑ
On-State Voltage (I _{TM} = 20 A Peak, Pulse Width ≤ 1 ms, Duty Cycle ≤ 2%)	V _{TM}	_	1.7	2.2	Volts
Gate Trigger Current, Continuous dc(2) $(V_D = 12 \text{ V, R}_L = 100 \Omega)$	^I GT	_	30	200	μΑ
Gate Trigger Voltage, Continuous dc $(V_D = 12 \text{ V}, \text{ R}_L = 100 \Omega)$ $(V_D = \text{Rated V}_{DRM}, \text{ R}_L = 10 \text{ k}\Omega, \text{ T}_J = 110^{\circ}\text{C})$	VGТ	 0.1	0.5 —	1.5 —	Volts
Holding Current (V _D = 12 V, I _{TM} = 100 mA)	lΗ	_	_	6	mA
Critical Rate of Rise of Forward Blocking Voltage (V _D = Rated V _{DRM} , T _J = 110°C, Exponential Waveform)	dv/dt	_	10	_	V/μs
Gate Controlled Turn-On Time $(V_D = Rated V_{DRM}, I_{TM} = 20 A, I_G = 2 mA)$	^t gt	_	1	_	μs

^{1.} Ratings apply for negative gate voltage or $R_{GK} = 1 \text{ k}\Omega$. Devices shall not have a positive gate voltage concurrently with a negative voltage on the anode. Devices should not be tested with a constant current source for forward and reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.



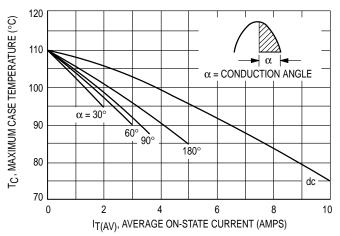


Figure 1. Average Current Derating

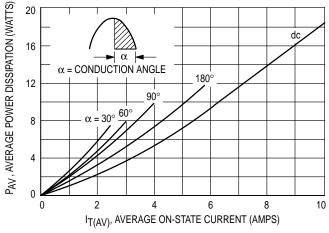


Figure 2. On-State Power Dissipation

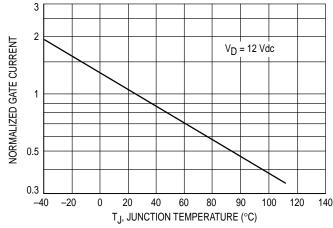


Figure 3. Normalized Gate Current

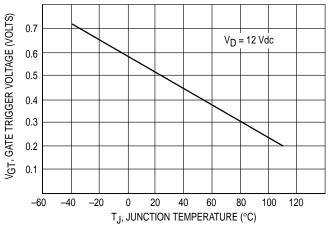
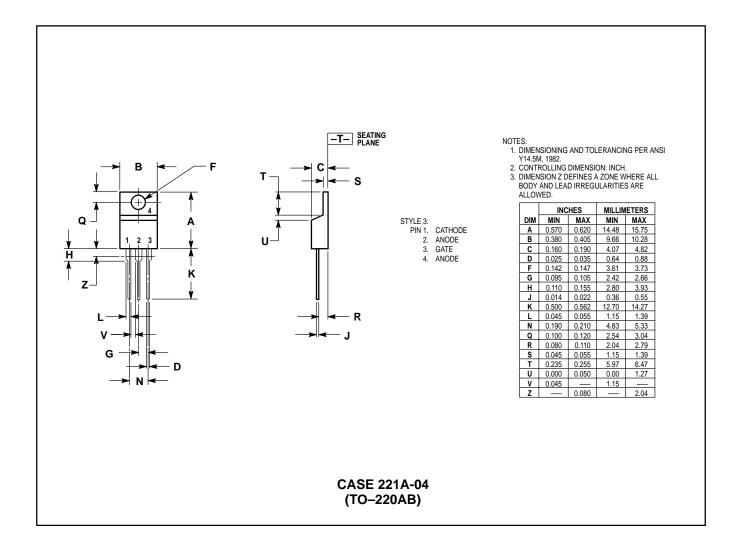


Figure 4. Gate Voltage

PACKAGE DIMENSIONS



MCR310 Series

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